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		Paper entitled "Submission of Prior Art Under 37 CFR 1.501, 6 pages. 3-22-2007.									
		Presentation slides entitled System-on-chip (SOC) and its Effect on Microcomputer Bus Products, as presented to the VMEbus Standards Organization (VSO) on July 21, 1999, in Vancouver, B.C., Canada, 31 pages.									
_		Technical reference manual entitled VME64 to PCI Bridge System-on-Chip (SoC), first published by Silicore									
		Corporation on December 7, 2002, 129 pages.									
		VHDL source code files representing the VMEcore entity and testbench, as produced by Silicore Corporation's parametric core generator known as The VMEbus Interface Writer TM on February 14, 2002, 46 pages.									
_	4	VHDL source code files	representing	the VME	core entity as distrib	uted with th	e VME64 to	PCI Bridge	System-c	n-Chip	
	П	(SoC), and described at	§4.7.3 VHD	L Synthesi	s and Test (p. 81) in	the technica	l reference n	nanual, 31 pa	ges. 2-	14-20	
		WISHBONE System-on	-Chip (SoC)	Interconn	ection Architecture f	or Portable	IP Cores, Re	vision B.2, o	lated Oct	ober 10,	
		2001, 109 pages. 5. Press release from Altium Ltd. (Sydney, Australia) entitled Altium Unveils new 'Board-on-Chip' Technology, dated									
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